

FIG. 1

RECEIVED IC 2, FIG.  
CLASS SURCLASS

## Receiving Workflow

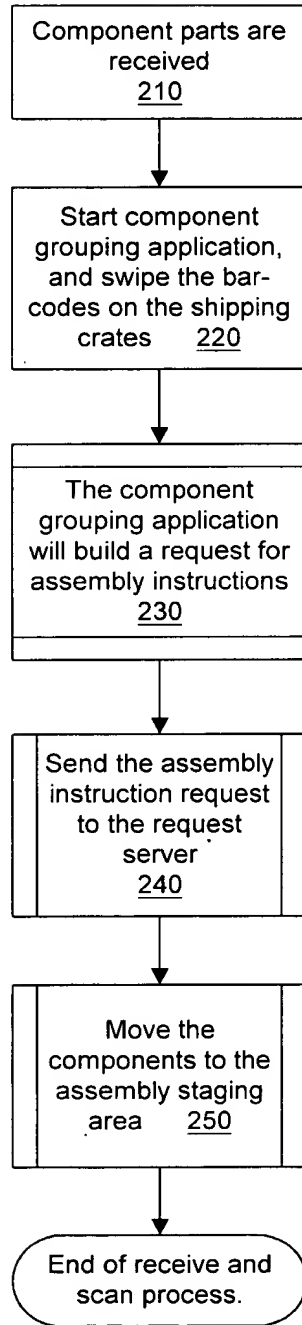
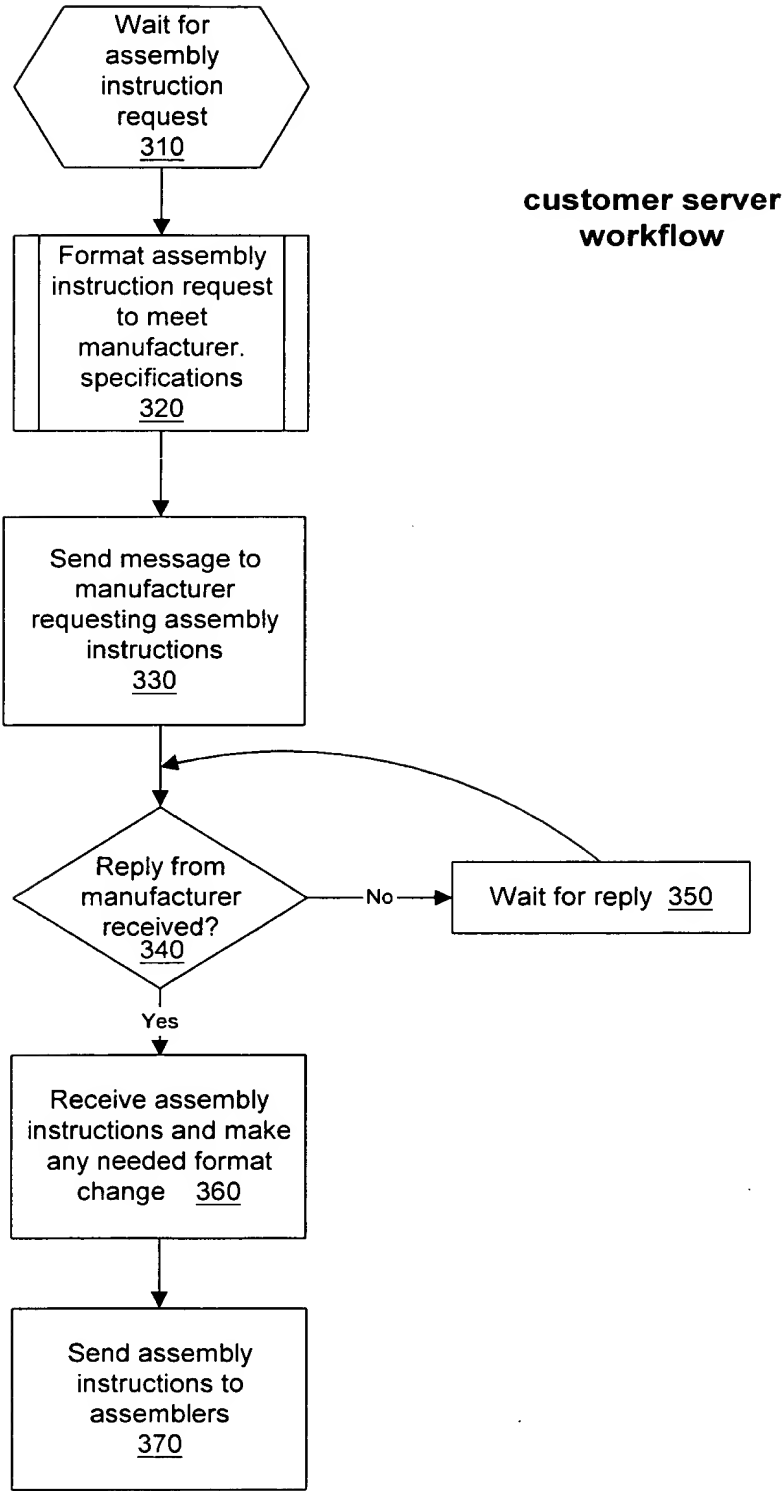


FIG. 2



**FIG. 3**

## Assembly workflow

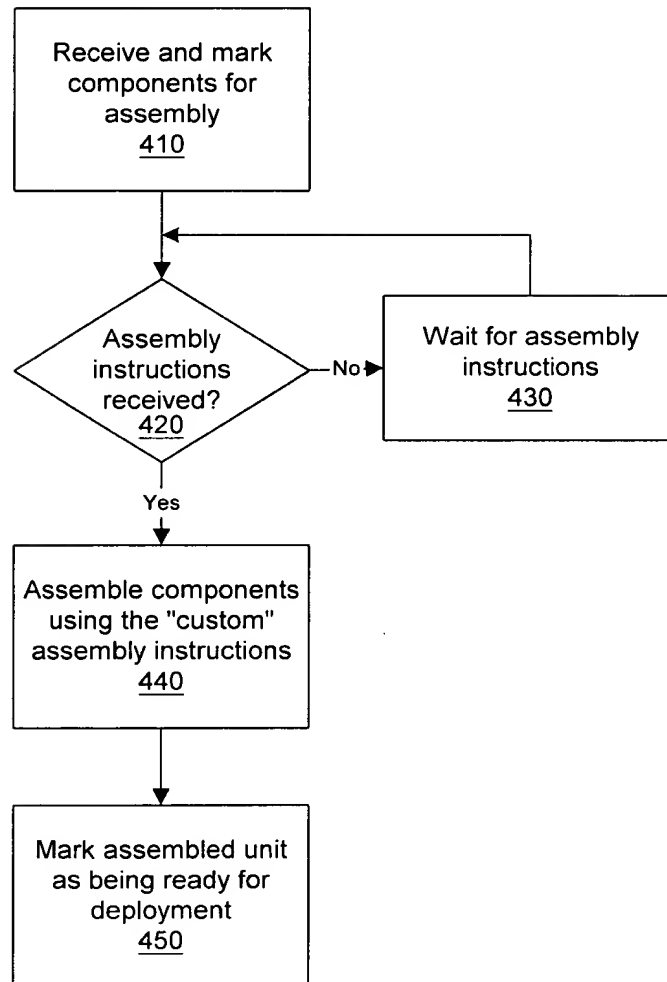
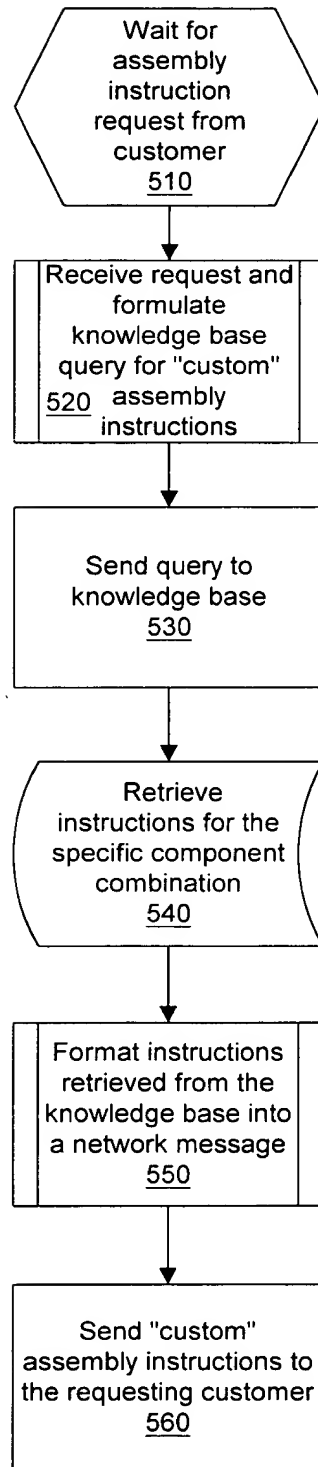
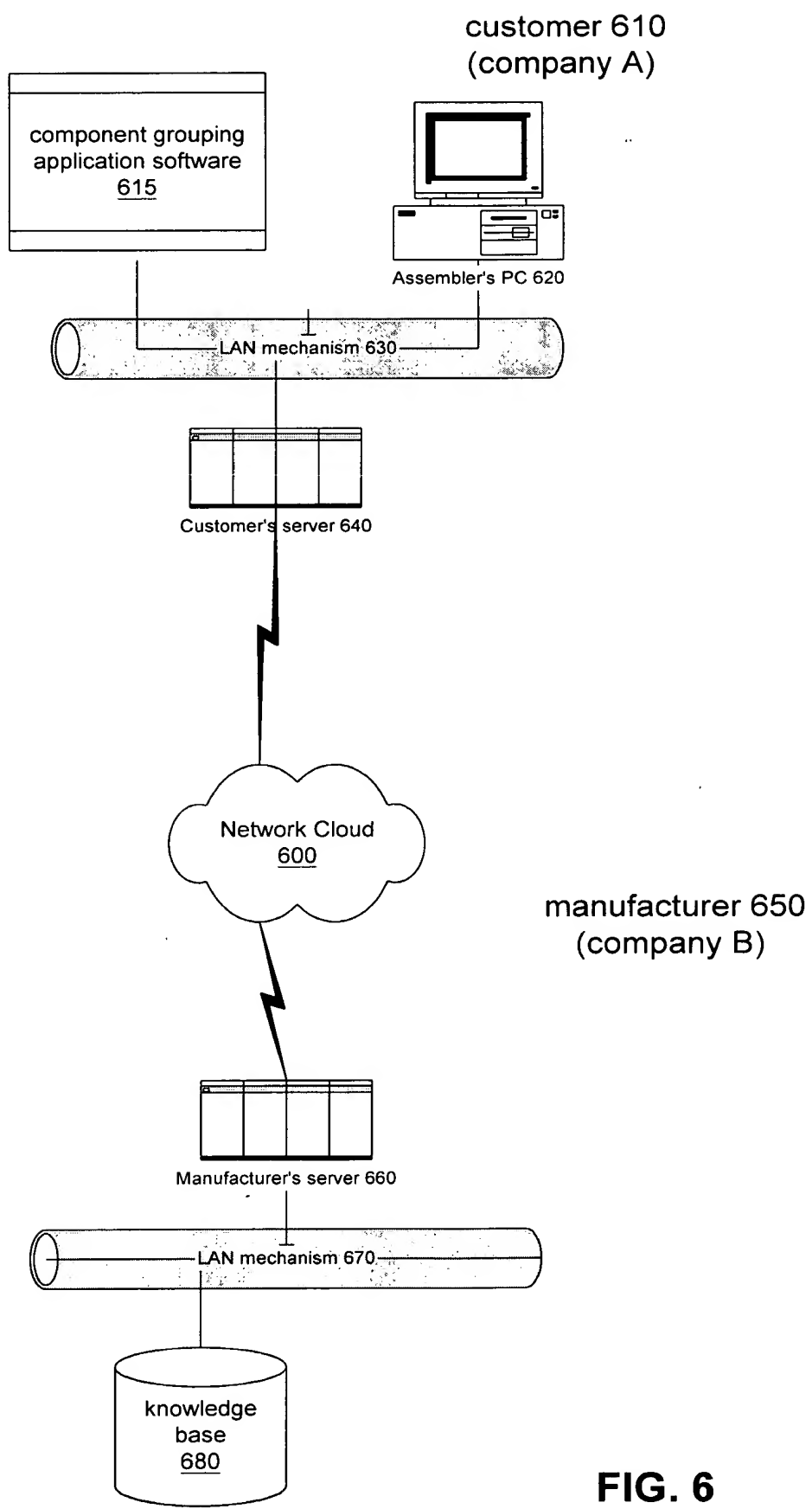


FIG. 4

**Manufacturer  
workflow**



**FIG. 5**



**FIG. 6**

computer system 707

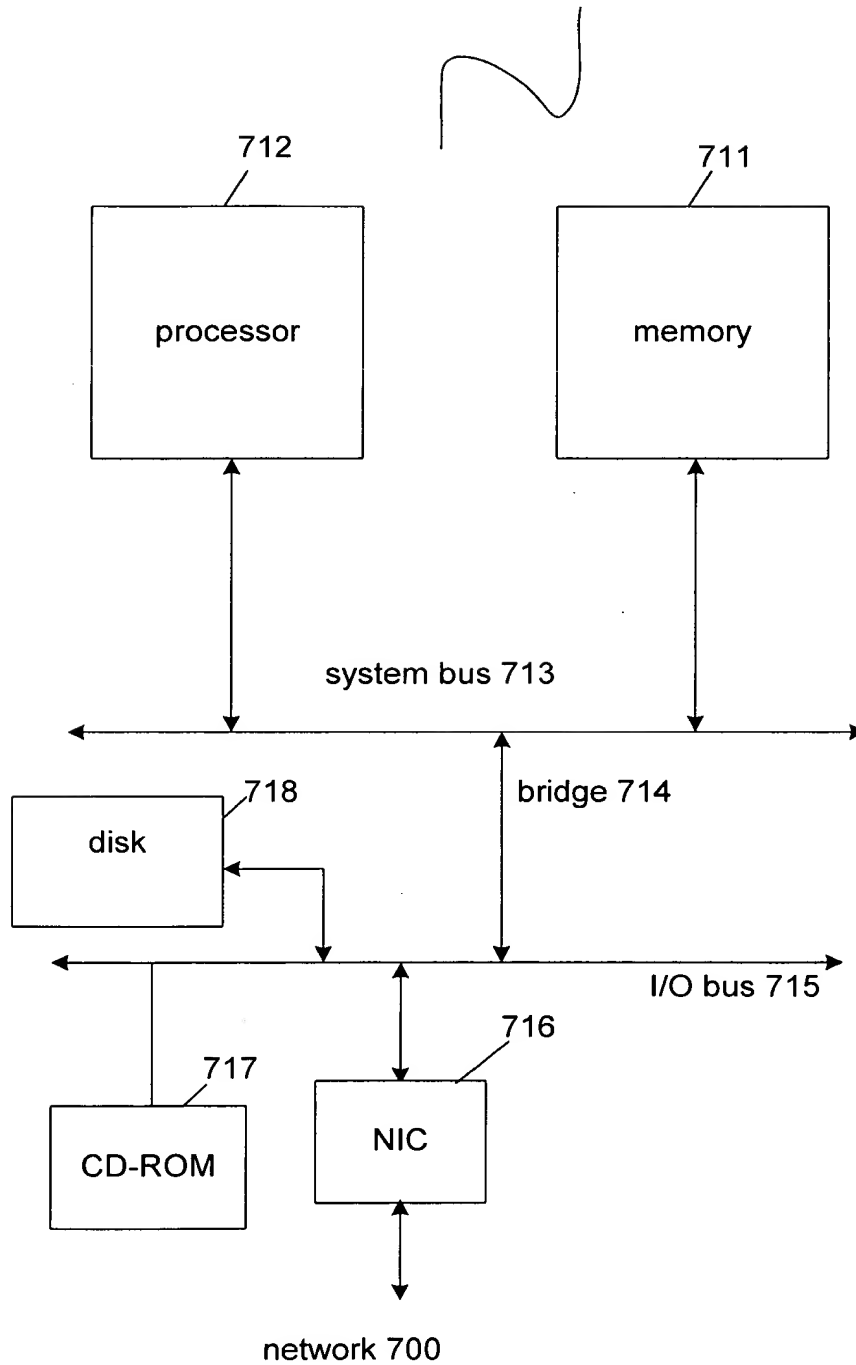


FIG. 7